

On the Performance of Large Intelligent Surfaces with LDPC Codes

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Abstract—This study explores the integration of Low-Density Parity-Check (LDPC) coding with Large Intelligent Surface (LIS) systems in conjunction with Single Carrier with Frequency Domain Equalization (SC-FDE). LIS systems represent a revolutionary advancement in wireless communication, leveraging densely packed antenna arrays for enhanced signal processing and beamforming capabilities. The research evaluates the performance of four receiver configurations: Maximum Ratio Combining (MRC), Equal Gain Combining (EGC), Zero Forcing (ZF), and Minimum Mean Squared Error (MMSE). By incorporating LDPC codes, substantial improvements in error correction and system reliability are achieved. Specifically, simulation results indicate a performance gain of approximately 3 dB for a 4×25 LIS system and 2 dB for a 4×225 LIS system when compared to uncoded systems.

Keywords: Large Intelligent Surface, LDPC Codes, SC-FDE, Wireless Communications, Receivers, Beamforming, Error Correction.

1. INTRODUCTION

Future communications will be intelligent and interactive, making artificial surfaces electronically active to enhance wireless links. The rise of the Internet of Things (IoT) and billions of connected devices pose communication challenges [1]. Massive Multiple-Input Multiple-Output (m-MIMO), Ultra Massive MIMO (UM-MIMO), and Extremely Large Antenna Arrays (ELAA) have transformed the field, boosting data rates and network capacity. The Large Intelligent Surface (LIS) concept extends m-MIMO, further increasing the number of antennas.

Wireless communications typically occur in the far-field, beyond the Fraunhofer distance. LIS, composed of multiple panels with several antenna elements, operates in the near-field, focusing not just on direction and elevation but also on distance [2,3]. This reduces interference between aligned users at different ranges, improving beamforming efficiency. Antenna spacing is $\lambda/2$, and element correlation enables beam formation. A block transmission strategy, including Single Carrier with Frequency Domain Equalization (SC-FDE) [4,5], is applied to LIS. This paper explores integrating Low-Density Parity-Check (LDPC) codes into this system.

High-frequency transmission faces challenges like free-space path loss and power amplification issues. LIS mitigates these limitations with compact antenna aggregates and multiple panels [6], leveraging reflection effects to enhance coverage [7]. This could exponentially increase wireless network capacity [8]. As an extension of m-MIMO, LIS enables short-range communication, reducing energy consumption and interference [1,6]. This study compares four receivers: Zero Forcing (ZF), Maximum Ratio Combining (MRC), Equal Gain Combining (EGC), and Minimum Mean Squared Error (MMSE) [6,9]. Integrating LDPC codes with SC-FDE reduces the Bit Error Rate (BER) [10,11], the paper's key contribution.

LDPC codes are highly efficient, approaching Shannon capacity with low decoding complexity [12]. They outperform other error-correcting codes in handling errors and computational efficiency.

2. SYSTEM AND SIGNAL MODEL

As shown in Figure 1, the n th transmitted block of N symbols from the t th user equipment (UE) is denoted as $s_n^{(t)}$, while the r th antenna of the LIS system receives $y_n^{(r)}$. Communication occurs in the near-field rather than the far-field.

The frequency-domain representation of the transmitted block is obtained via the Discrete Fourier Transform (DFT):

$$DFT\{s_n^{(t)}; n = 0, 1, \dots, N - 1\} = \{S_k^{(t)}; k = 0, 1, \dots, N - 1\} \quad (1)$$

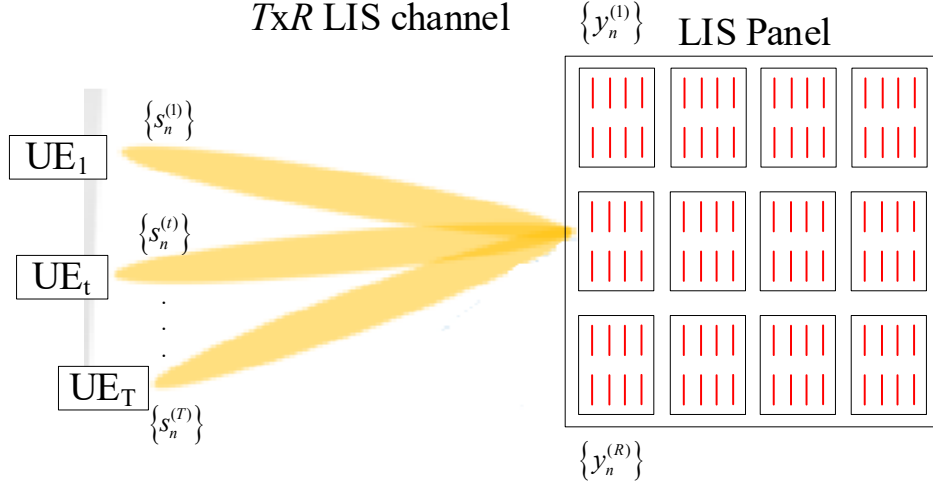


Figure 1. Block diagram of an LIS System.

The received block undergoes a similar transformation:

$$DFT\{y_n^{(r)}; k = 0, 1, \dots, N - 1\} = \{Y_k^{(r)}; k = 0, 1, \dots, N - 1\} \quad (2)$$

After cyclic prefix removal, assuming it exceeds the channel impulse response, the received frequency-domain signal is given by [13]:

$$Y_k = [Y_k^{(1)}, \dots, Y_k^{(R)}] = H_k S_k + N_k \quad (3)$$

where $S_k = [S_k^{(1)}, \dots, S_k^{(T)}]^T$ represents the transmitted symbols in the frequency domain, and H_k it the $T \times R$ channel matrix for the k th subcarrier, with its (r, t) th element denoted as $H_k^{(t,r)}$. The channel's frequency response is:

$$\{H_k; k = 0, 1, \dots, N - 1\} = DFT\{h_n; n = 0, 1, \dots, N - 1\} \quad (4)$$

Lastly, N_k represents the noise in the frequency-domain for the k th subcarrier [14].

ZF and MMSE receivers are computationally intensive, requiring channel inversion for each frequency component, leading to substantial battery consumption. The ZF receiver efficiently eliminates intersymbol interference but suffers from noise amplification, degrading performance in moderate to high noise scenarios. In contrast, MRC and EGC receivers are less complex, making them suitable for LIS deployments. However, they introduce some interference during decoding, which can be mitigated with iterative receivers.

For a non-iterative receiver, the estimated frequency-domain data symbols, denoted as $\tilde{\mathbf{S}}_k = [\tilde{S}_k^{(1)}, \dots, \tilde{S}_k^{(R)}]^T$, are calculated as described in [14]. Specifically:

$$\tilde{\mathbf{S}}_k = \mathbf{B}_k \mathbf{Y}_k \quad (5)$$

Depending on the algorithm, $\mathbf{B}_k$ can be computed differently [14]. Using the ZF receiver, $\mathbf{B}_k$ comes

$$\mathbf{B}_k = \mathbf{H}_k^H (\mathbf{H}_k \mathbf{H}_k^H)^{-1}, \text{ while } \mathbf{B}_k \text{ comes } \mathbf{B}_k = (\mathbf{H}_k \mathbf{H}_k^H + \beta \mathbf{I})^{-1} \mathbf{H}_k^H \text{ for the MMSE, and where } \beta = \frac{\sigma_N^2}{\sigma_S^2} = \frac{\frac{E[|N_k|^2]}{2}}{\frac{E[|S_k|^2]}{2}}$$

and where $\mathbf{I}$ is an $R \times R$ identity matrix. Using the MRC receiver, $\mathbf{B}_k$ comes $\mathbf{B}_k = \mathbf{H}_k^H$ and $\mathbf{B}_k$ comes $\mathbf{B}_k = \exp\{j \arg(\mathbf{H}_k^H)\}$ for the EGC.

Iterative block decision feedback equalization (IB-DFE) [13] is a highly efficient receiver for single-carrier frequency-domain equalization (SC-FDE), utilizing feedforward and feedback coefficients for enhanced performance. While MRC and EGC are computationally simple, they generate residual interference, especially with moderate signal-to-noise ratio (SNR) values. This can be countered by iterative receivers, similar to IB-DFE, implementing the function:

$$\tilde{\mathbf{S}}_k = \mathbf{B}_k^H \mathbf{Y}_k - \mathbf{C}_k \tilde{\mathbf{S}}_k \quad (6)$$

where the interference cancellation matrix, $\mathbf{C}_k$, is computed as $\mathbf{C}_k = \mathbf{A}_k^H \mathbf{H}_k - \mathbf{I}$ [14], where $\mathbf{I}$ is an $R \times R$ identity matrix.

3. SIMULATION RESULTS AND ANALYSIS

Monte Carlo simulations assessed uplink BER performance in LIS systems with SC-FDE and LDPC codes, varying E_b/N_0 . QPSK modulation ($N=256$), rate $\frac{1}{2}$ LDPC codes (32,400 length), and five-path Rayleigh fading were used. ZF, MMSE, MRC, and EGC receivers were compared. ZF and MMSE, requiring channel inversion, are computationally expensive. Matched Filter Bounds (MFB) served as performance lower bounds, indicating near-optimal receiver performance.

Figure 2 displays the results with 4X25 LIS System, with five users, with and without LDPC codes. Regarding these outcomes, we observe that the use of LDPC codes results in a performance improvement of the order of 3 dB, for all receiver types.

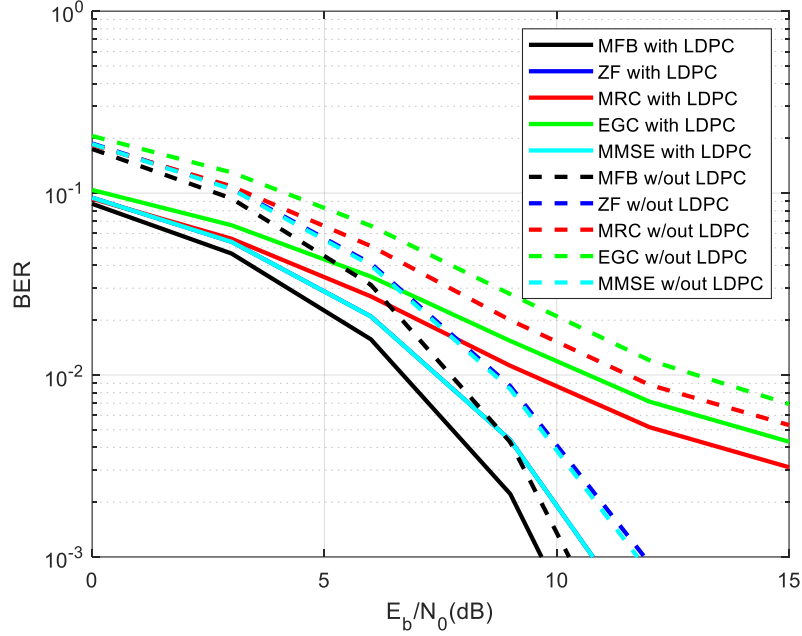


Figure 2. Results for 4X25 LIS System, with 5 users, with and without LDPC codes.

In Figure 3, the performance outcomes for a 4X225 LIS system, supporting five users, are presented, illustrating both coded and uncoded scenarios. Consistent with the trends observed in Figure 2, the implementation of LDPC codes yielded a performance improvement of approximately 2 dB compared to the uncoded configuration.

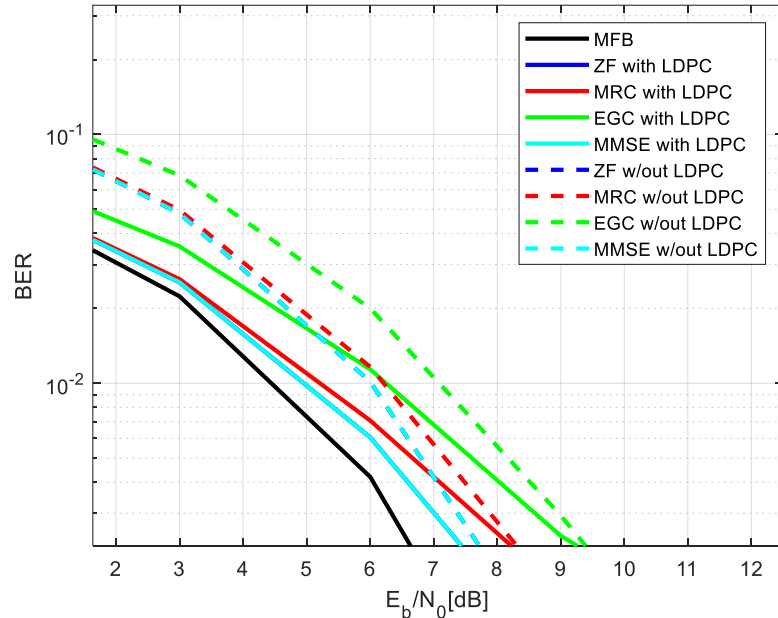


Figure 3. Results for 4X225 LIS System, with 5 users, with and without LDPC codes.

4. CONCLUSIONS

The findings underscore the efficiency of the MMSE receiver, which achieves the best overall performance due to its ability to mitigate interference. However, this comes at the cost of computational complexity. Alternatively, MRC offers a viable low-complexity solution while maintaining competitive results. The LIS system associated with the MRC receiver simplifies processing, further enhancing its feasibility for practical implementations. Additionally, the results demonstrate that the use of LDPC codes provides a significant performance gain compared to the uncoded system, improving BER by approximately 2-3 dB,

depending on the LIS configuration. This highlights the effectiveness of LDPC codes in enhancing system reliability.

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